



ECO CONSULTATION

MATERIAL SPECIFICATIONS:
NOTED

OTHER TREATMENTS AND FINISHES
NOTED

PAINT - COLOR
NONE

PART NAME:
CIRCUITBOARD [UI, LINUX, AM3]

GLOSS PERFORMANCE ACCEPTANCE

CHANGED BY:
BRETT PAULSEN
DATE:
12/10/2025

MDR:
STACY GAHLON
10/13/2023

DES:
BRETT PAULSEN
06/23/2021

REV
03

ECO

WELDING NOTATION IN ACCORDANCE
WITH AWS A2.4-98
UNLESS OTHERWISE SPECIFIED
DIMENSION TOLERANCING IN
ACCORDANCE WITH ASME Y14.5M-2009
ALL UNTOLERANCED DIMENSIONS ARE
BASIC AND CONTROLLED BY:

X.X ± 0.8 $\pm [0.03]$
X.XX ± 0.25 $\pm [0.010]$
X.XXX ± 0.125 $\pm [0.0049]$
ANGLES $\pm 0.5^\circ$

GENERAL NOTES
PRIMARY DIMENSIONS ARE METRIC. REFERENCE
DIMENSIONS WITH BRACKETS ARE INCH. UNLESS
OTHERWISE SPECIFIED ALL DIMENSIONS ARE
AFTER TREATMENTS AND FINISHES.

PROPRIETARY INFORMATION
MAY NOT BE REPRODUCED OR DISCLOSED
TO OTHERS WITHOUT WRITTEN PERMISSION
OF TENNANT COMPANY.

DWG
B
SIZE
PART NUMBER
1261318

TENNANT

REVISIONS				
ZONE	REV.	DESCRIPTION	DATE	APPROVED
C5, D5	03	1) CHANGE BOM TO BOM 1261318revG 2) UPDATE HARDWARE REVISION 3) CHANGE FCT TO FCT_AM3_LINUX_UI_rev18	12/10/2025	

- 03 NOTES:
- 1) PCB ASSEMBLY TO BE BUILT USING TENNANT QPL BILL OF MATERIAL: BOM1261318revG
 - 2) WORKMANSHIP STANDARD PER: IPC-J-STD-001E, IPC-A-610E, CLASS 2
SOLDERING AND ELECTRICAL CONNECTIONS : IPC-S-815B
OR MORE CURRENT VERSIONS OF THESE STANDARDS
 3. APPLY LABEL OR LABELS WITH THE FOLLOWING INFORMATION:
HUMAN READABLE TENNANT PART NUMBER AND REVISION
NOTE: THIS NUMBER AND REVISION SHOULD MATCH THIS RELEASED TENNANT DRAWING
HUMAN READABLE MANUFACTURER'S LABEL WITH SERIAL NUMBER
BARCODE CONTAINING SERIAL NUMBER
NOTE: LINEAR OR 2D BARCODE ACCEPTABLE
NOTE: 2D MATRIX DIMENSIONS TO BE NO SMALLER THAN 4MM BY 4MM
PLACE LABELS SO THEY ARE VISIBLE FROM THE TOP OF THE ASSEMBLY
 - 4) CONFORMAL COATING TO BE APPLIED TO TOP AND BOTTOM OF THE PCB
 - ALL MOUNTING HOLES TO BE MASKED OFF AT A MIN DIAMETER OF 9MM
 - NO COATING TO BE APPLIED TO CONNECTORS J1, J3, J4, J5, J6, J7, J8, J9, J10, J11, J12, J13, J14, J18, S1, S2, S3, S4
 - COATING TO FOLLOW QPL Q50000-001
 - 03 5) EACH ASSEMBLY TO BE ICT AND FUNCTIONALLY TESTED
FUNCTIONAL TEST PER SPEC: FCT_AM3_LINUX_UI_rev18
HARDWARE REVISION (FOR REFERENCE ONLY)
 - DECIMAL: 4.0
 - BINARY: 0100
 - 6) ASSEMBLY TO BE MANUFACTURED PER RoHS/LEAD FREE STANDARDS
 - 7) REFER TO PCB MANUFACTURING NOTES ON FABRICATION LAYER OF GERBERS
 - 8) ALL DIMENSIONS ARE FOR REFERENCE ONLY
 - 9) U20 TO BE SET TO SINGLE-LEVEL-CELL MODE AND PROGRAMMED WITH
FIRMWARE Q30109-010_am3-commercial_1_9_0_251.img.gz
U28 TO BE PROGRAMMED WITH FIRMWARE Q30120-002_am3-bootloader.img
 10. APPLY NON-CORROSIVE PCB-SAFE RTV AROUND C193, L5, AND B2.
RTV TO FOLLOW QPL Q50001-001, MINIMUM BEAD DIAMETER 3MM