



INTEGRATED STANDARD USER INTERFACE

Commercial Base model User Interface Board

Adapted from Volga Wet base model User Interface Board

Changelog:

Q26034-003
REV A -- Updates:
Based off Q26034-002 Design.
Added Accelerometer

1231461 No Telemetry (Variant = PopulateProd)

1232996 Telemetry (Variant = PopProdTelem)

1261079 Telemetry 4G NA

1261182 Telemetry 4G EMEA

REFERENCE PART NUMBERS:
Referenced from - 1219795 Rhine UI



FW1
FIRMWARE

GET UPDATED FIRMWARE INFO FROM
LATEST DRAWING: 1231461

PCB1
PCB

Q25082-003

SCHEMATIC1
SCHEMATIC

Q26034-003



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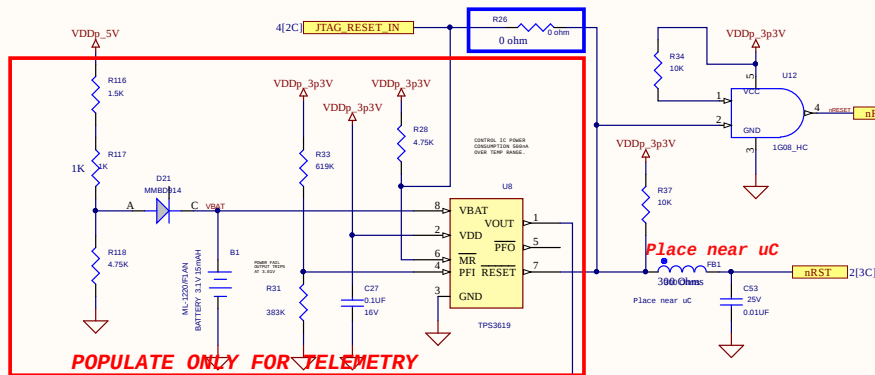
MICROCONTROLLER HARDWARE

ONLY CHARGES BATTERY
WHEN MACHINE IS
RUNNING.
ONLY USES BATTERY
WHEN MAIN BATTERY IS
DISCONNECTED.

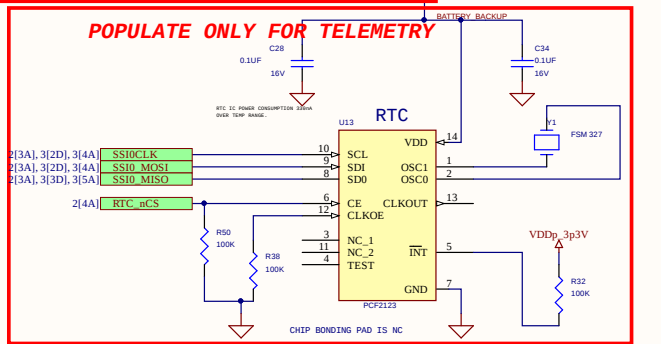
PLACE NEAR MICROCONTROLLER!

BATTERY AND RESET

POPULATE ONLY FOR NO-TELEMTRY

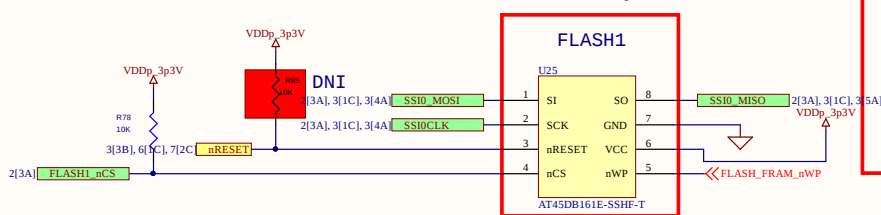


POPULATE ONLY FOR TELEMTRY



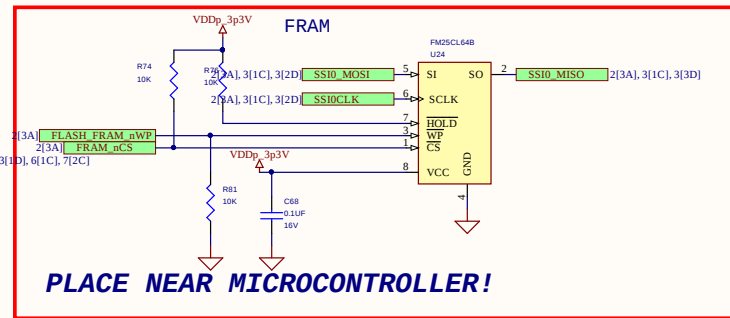
PLACE NEAR MICROCONTROLLER!

CHANGED: From MT25QL128ABA1ESE



DONE

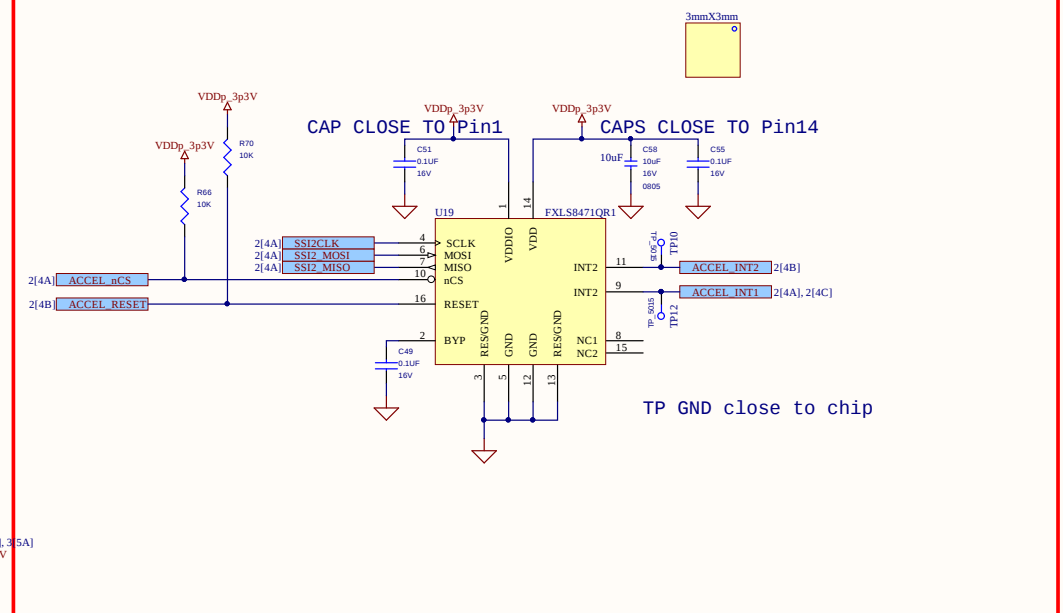
FM25CL64B



PLACE NEAR MICROCONTROLLER!

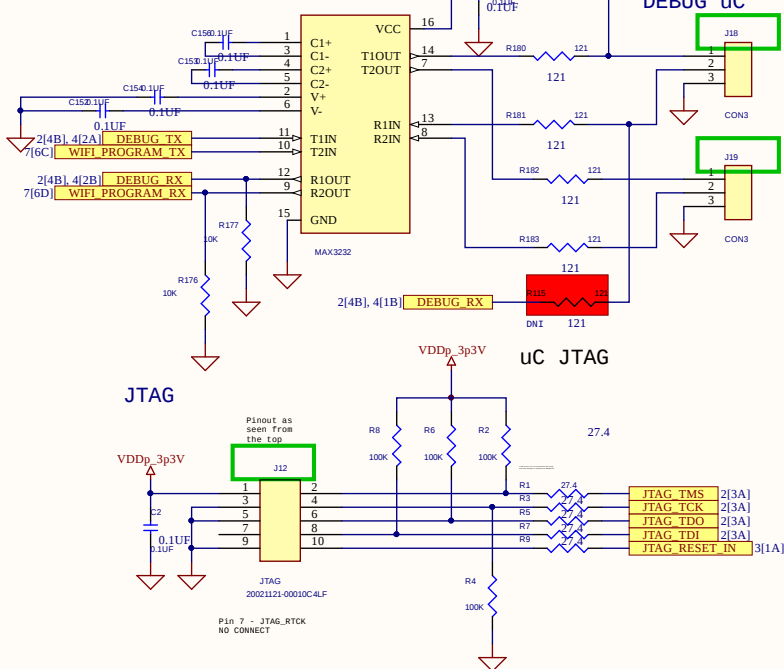
ACCELEROMETER

ORIENTATION TOP RIGHT



TP GND close to chip

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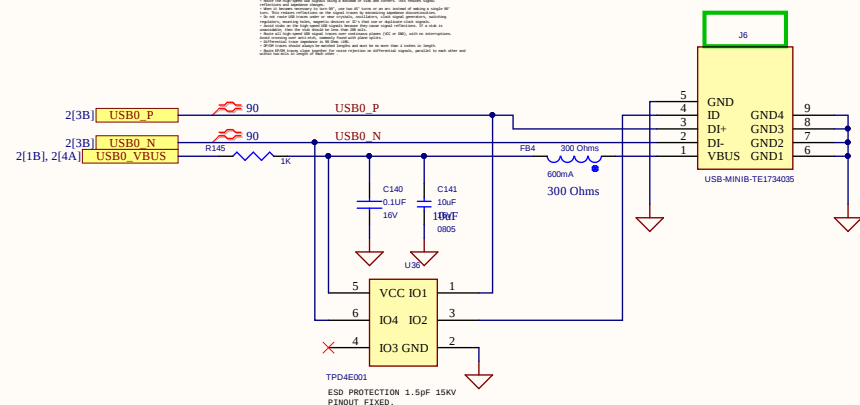


CONNECTORS

The diagram illustrates a CAN bus termination circuit. It features two 74AHC1G-series ICs: a 74AHC1G86 (U15) and a 74AHC1G125 (U10). The 74AHC1G86 is configured as an OR gate, with its inputs (pins 1 and 2) connected to the CAN0_TX and CAN0_RX signals through a 10K resistor (R39). The output (pin 4) of the 74AHC1G86 is connected to the VCC pin (pin 2) of the 74AHC1G125. The 74AHC1G125 is also connected to VDDp_3p3V (pin 5), GND (pin 3), and its output (pin 1) is connected to the CAN0_RX signal through a 10K resistor (R30). The circuit is powered by VDDp_5V and VDDp_3p3V.

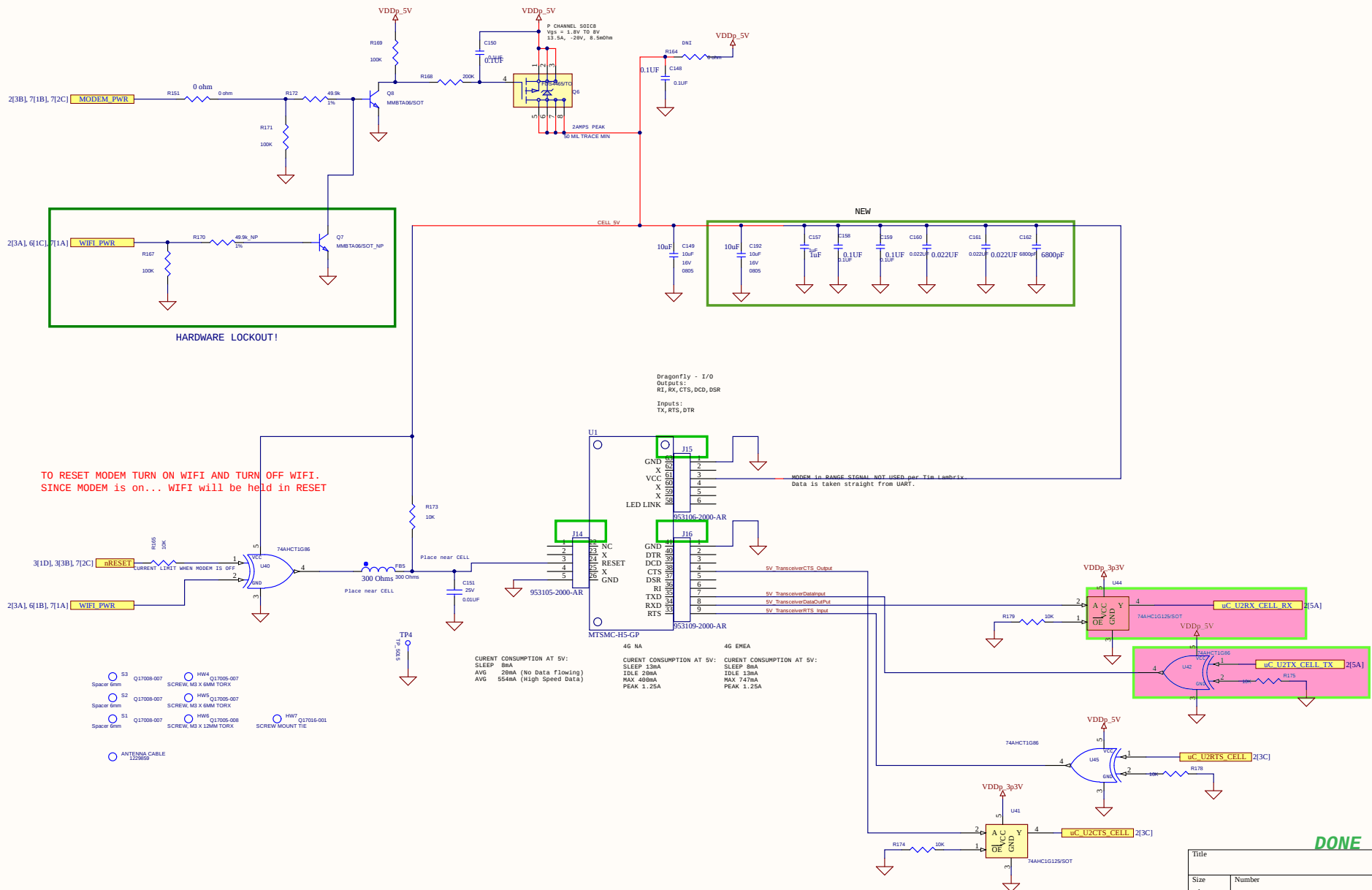
[illegible]

47nF
0.16hms
62

[illegible]

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CELL MODEM

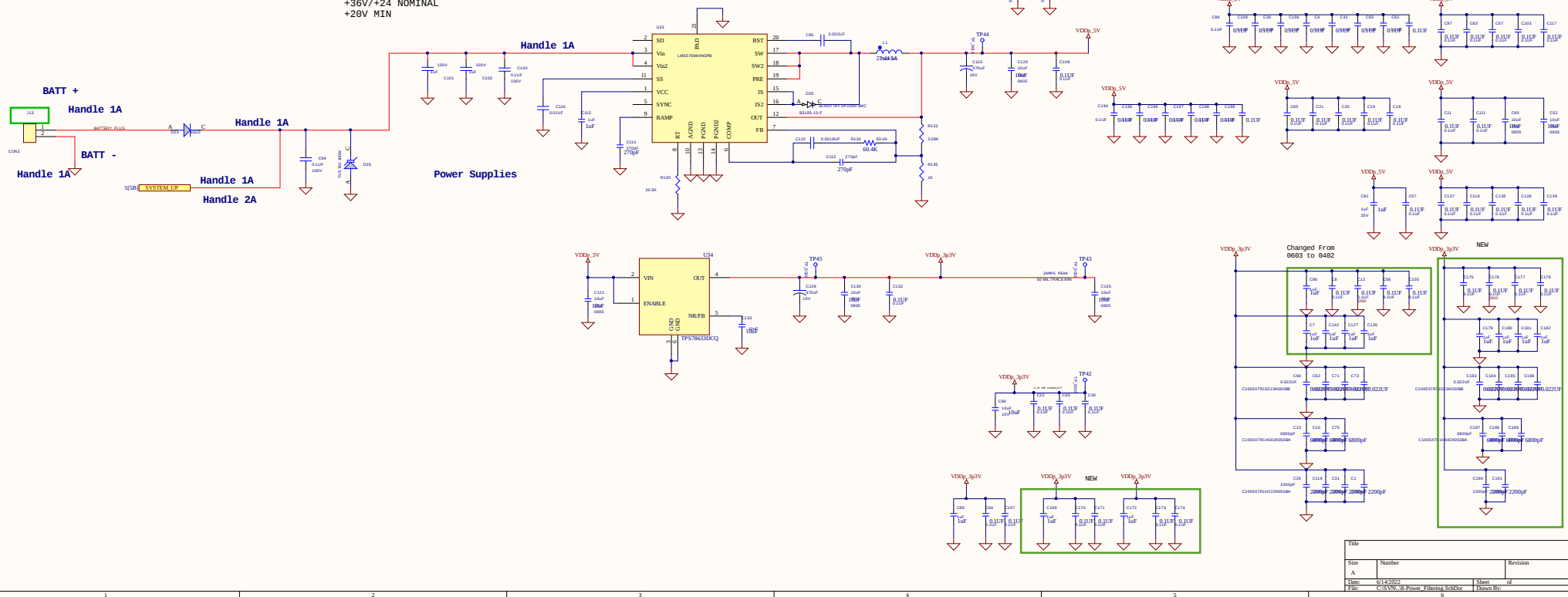


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POWER INPUT
52.2V MAX.
+36V/+24 NOMINAL
+20V MIN

POWER SUPPLY +5V, 75VIN, 3A



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