



Cassoday ECH20 - LM4 Board

Changelog:

Rev A:

- Initial Release.
- Design starts with 1212102, changes processor to LM4, adds accurate A/D reference.

Rev B:

- Fix Alex Stephens' wrong GPIO choices.
- Add revision select pullups.
- P1 boards.

Rev C:

- Remove test points: TP1, TP5, TP8-TP12, TP23, TP26.
- Change C38, C65, C74 to ceramic.
- Move LEDs to PP0 and PP1.
- PP boards, Q25066-003.

Rev D:

- Change U28-77 Vbat connection to processor to Vdd.
- Prod boards, Q25066-004.

Rev E:

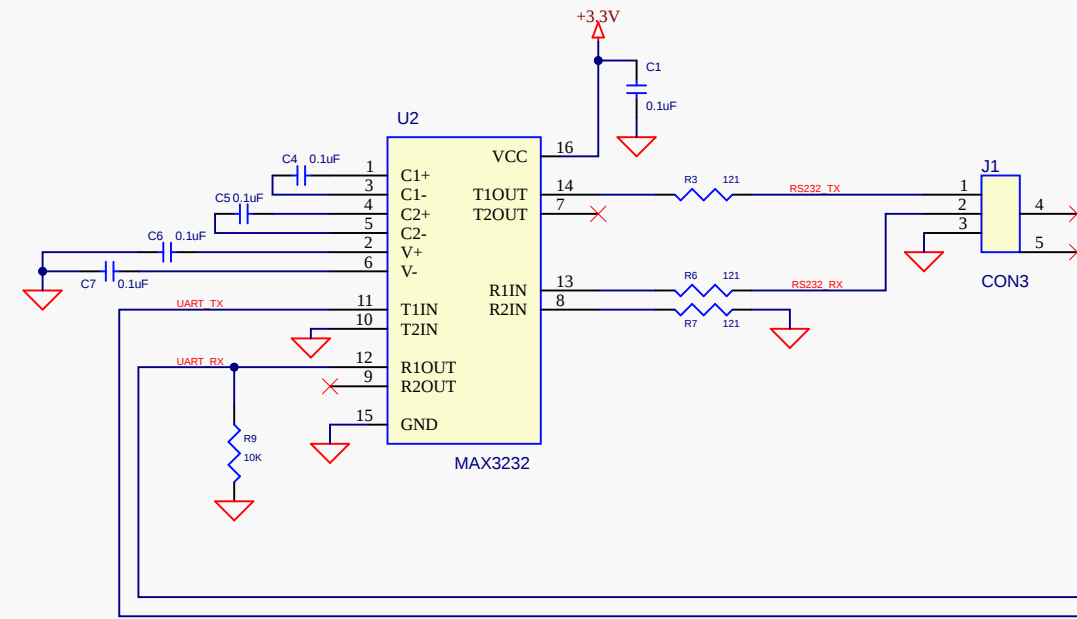
- Converted from OrCAD to Altium.
- Changed R113, R114, R117, R118, R136, R137, R140, and R141 to be 100 ohm resistors.

FIDUCIAL/A
FID1
FIDUCIAL/A
FID2
FIDUCIAL/A
FID3

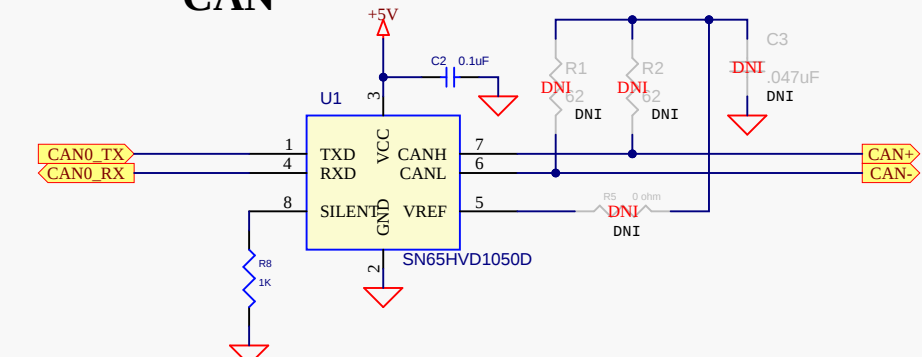
- ☐ FW
FIRMWARE Q30069-001
- ☐ PCB1
PCB Q25066-PCB_revE
- ☐ SCHEMATIC1
SCHEMATIC Q26018-SCH_revE

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		Size: B	CASSODAY LM4 ECH20 MODULE	
			T12 REFRESH ECH20	
		Document Number Q26018-SCH_revE	Engineer Anh Lam	
		Date: 11/18/2024	Sheet 1	of 5

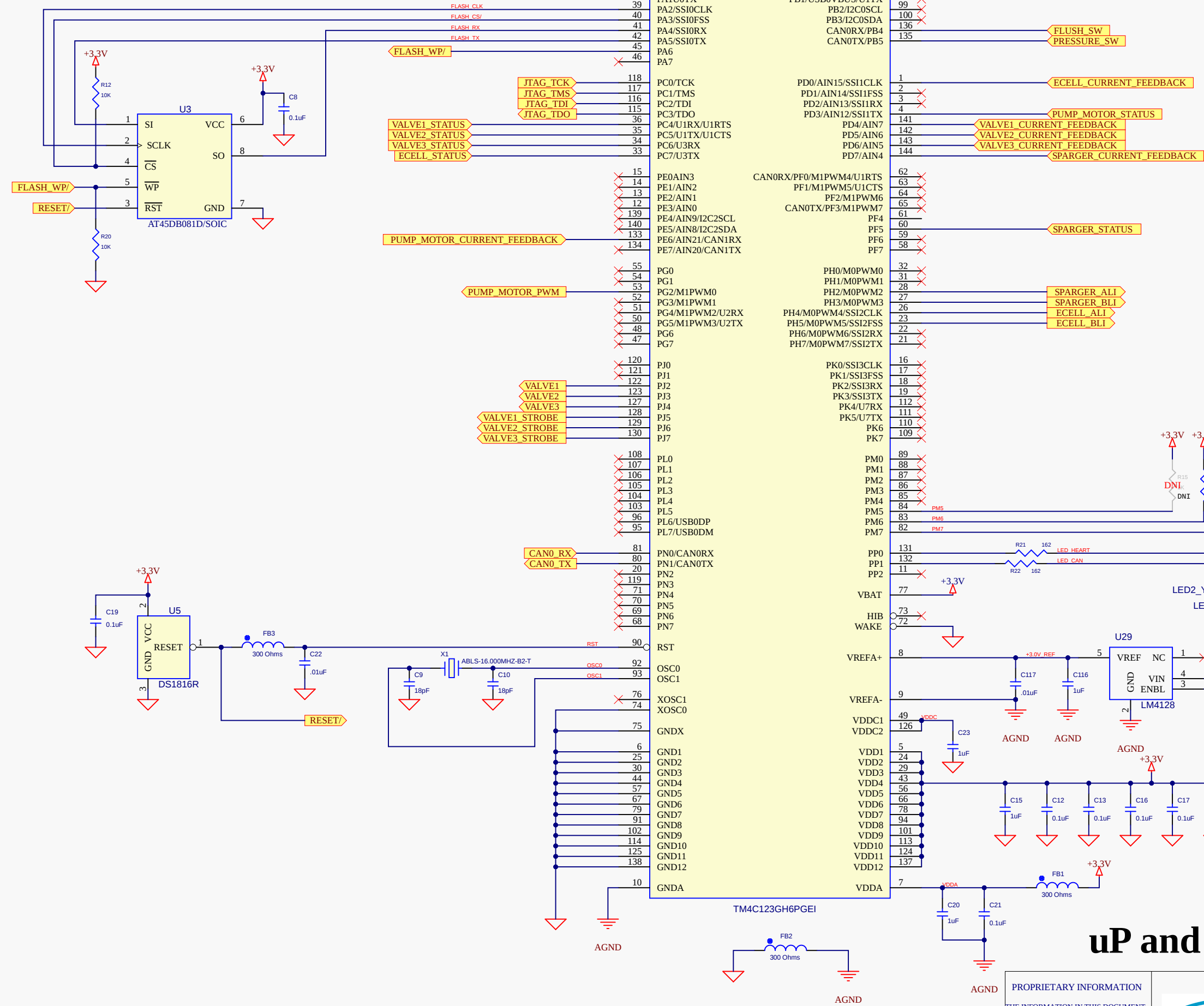
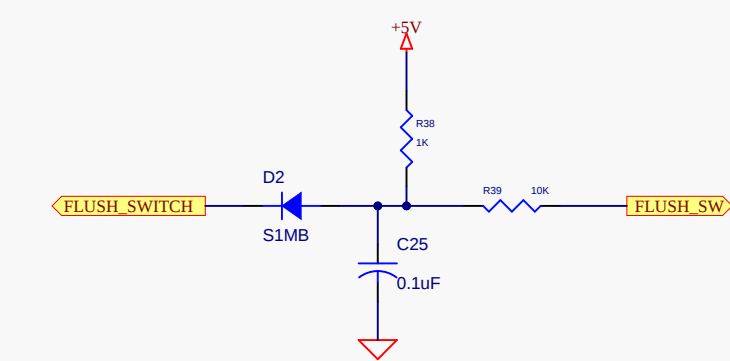
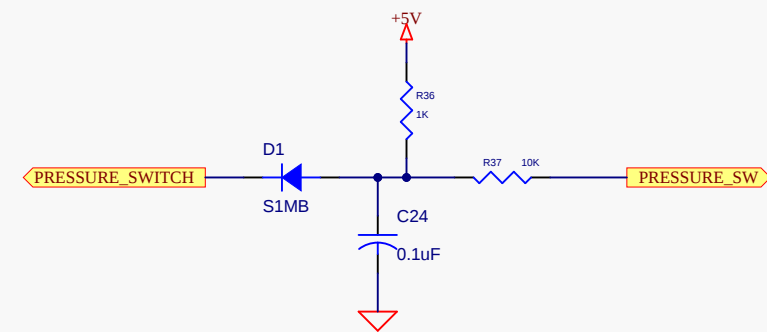
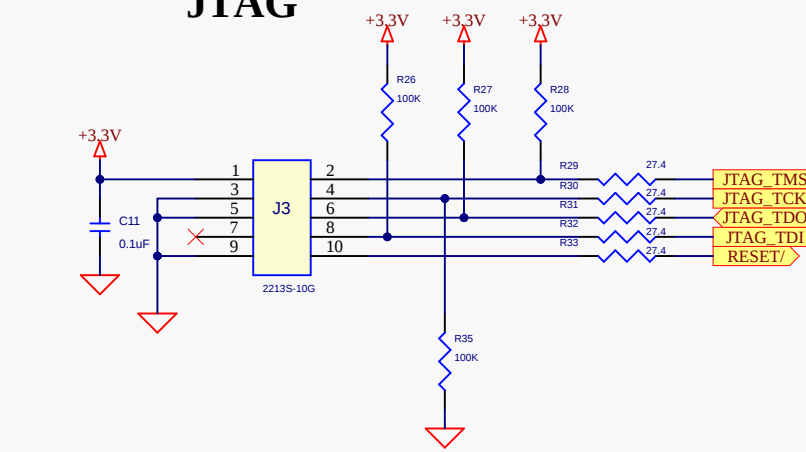
RS-232 SERIAL PORT



CAN

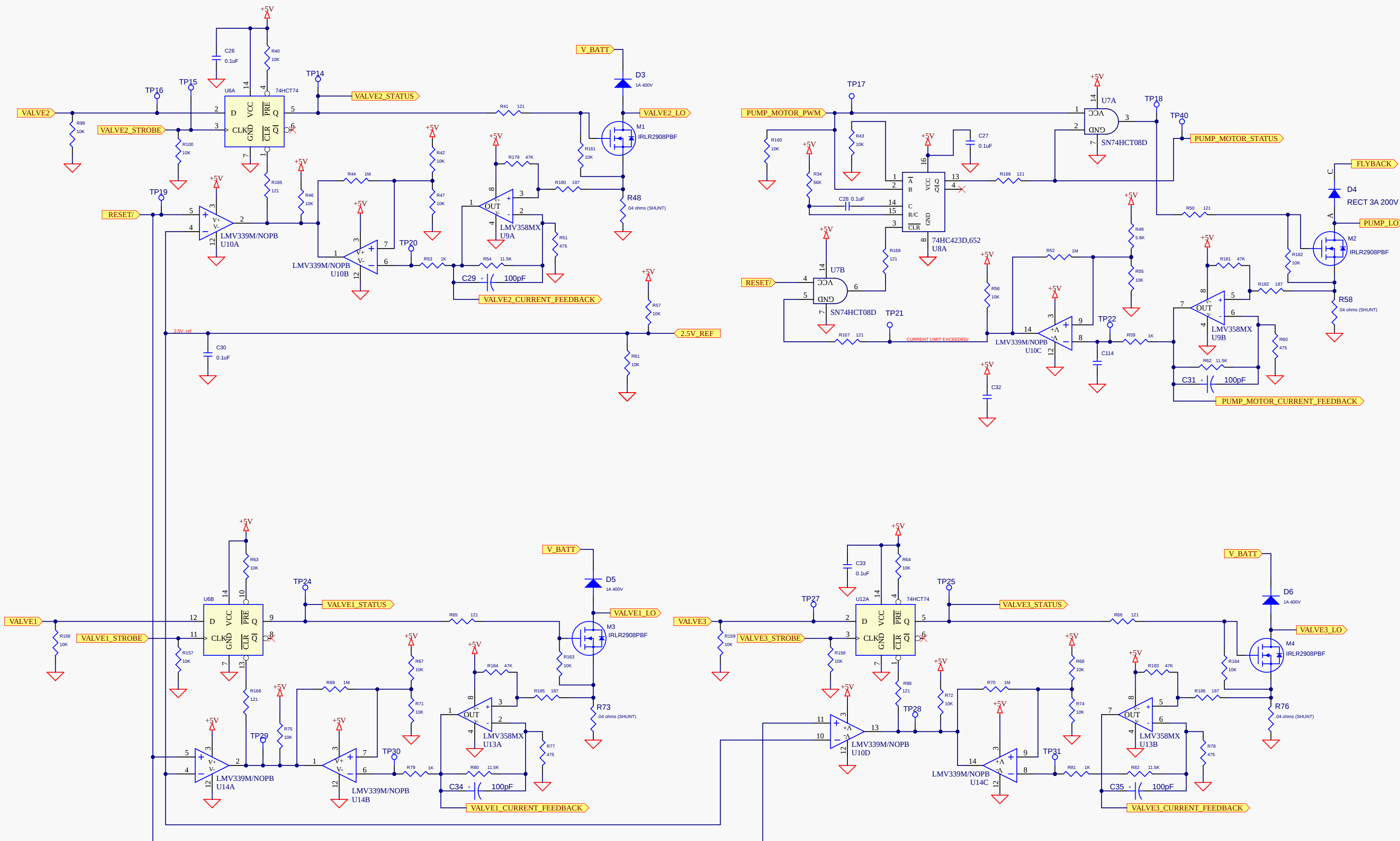


JTAG



uP and Communication

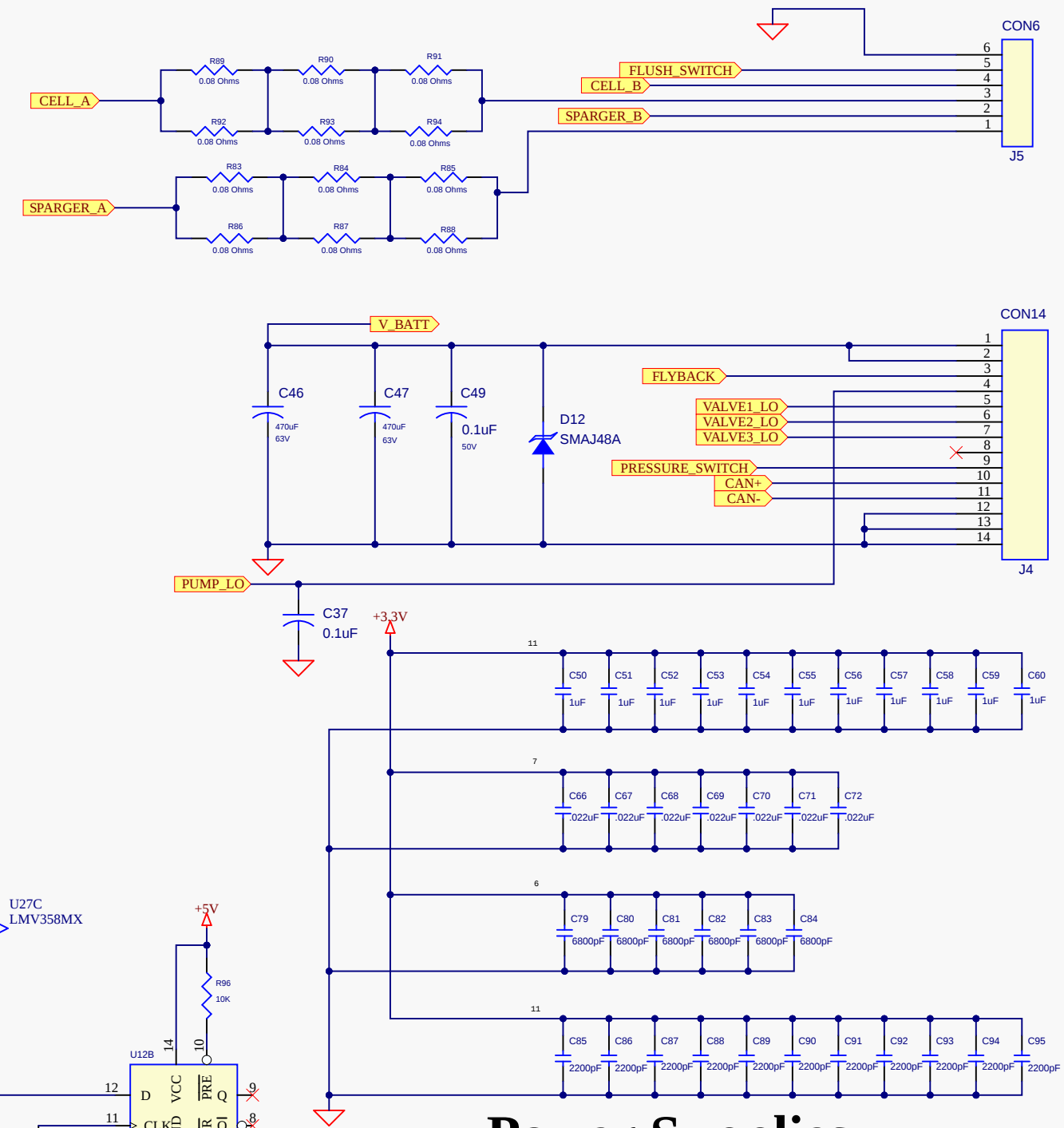
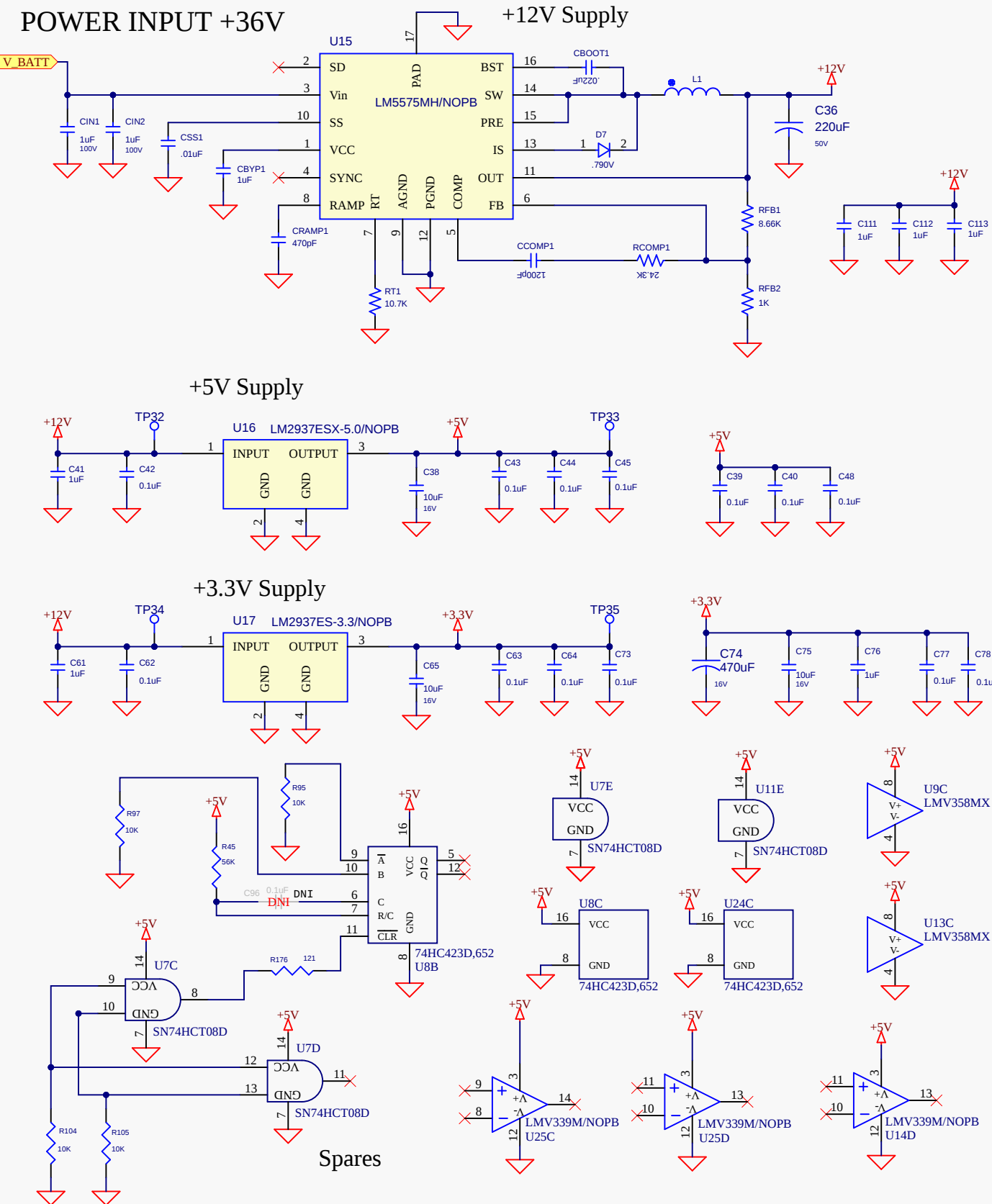
Drivers



Drivers

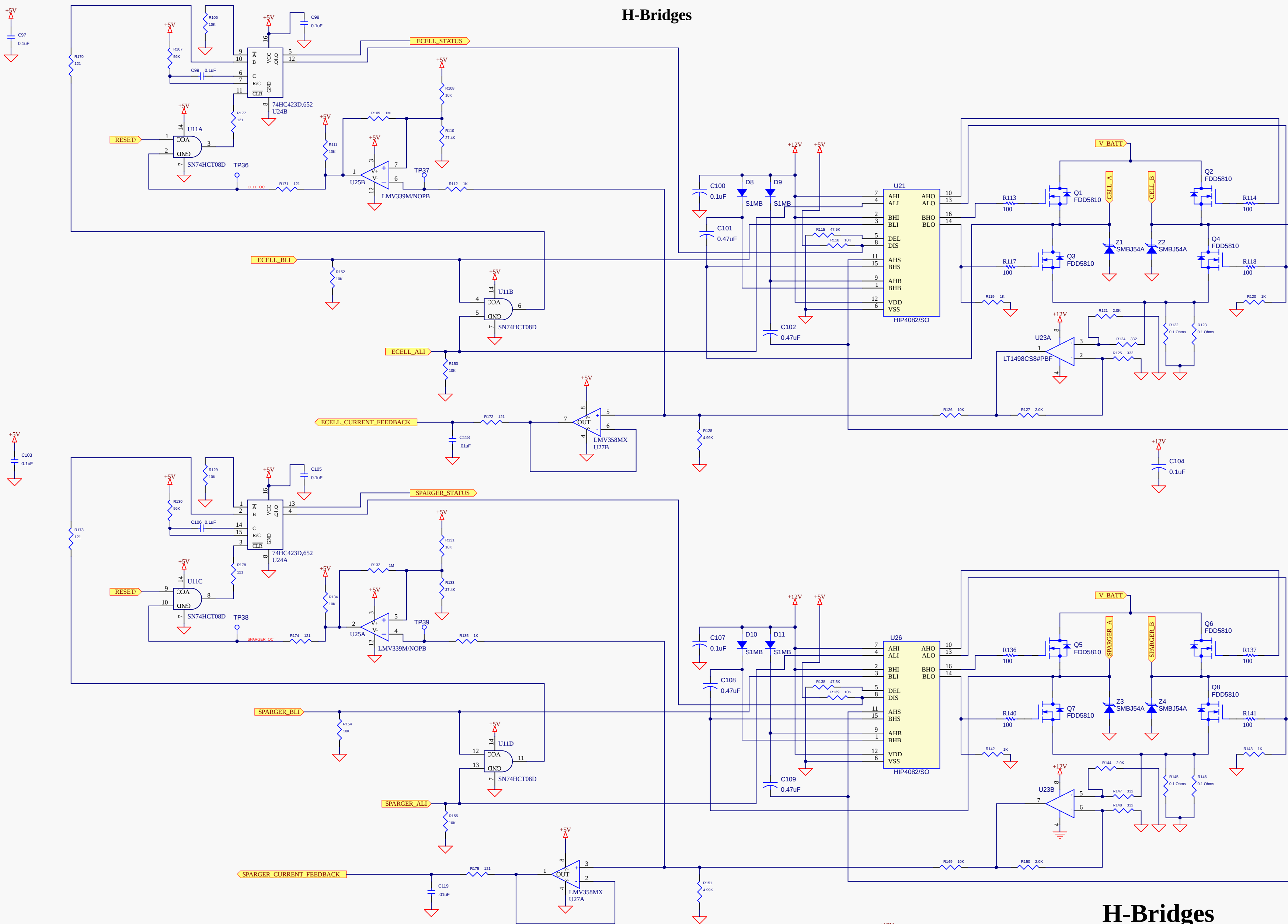
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Power Supplies



H-Bridges

H-Bridges



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